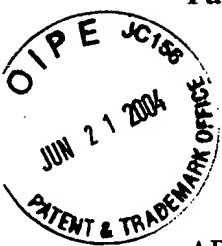


Patent

Attorney Docket No.: Intel 2207/10122

Assignee: Intel Corporation



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Sailesh B. KOTTAPALLI  
SERIAL NO. : 09/753,764  
FILED : December 29, 2000  
FOR : METHOD AND APPARATUS FOR REDUCING  
COMPONENTS NECESSARY FOR INSTRUCTION  
POINTER GENERATION IN A SIMULTANEOUS  
MULTITHREADED PROCESSOR  
GROUP ART UNIT : 2183  
EXAMINER : David J. HUISMAN

**RECEIVED**

JUN 25 2004

Technology Center 2100

M/S: NON-FEE AMENDMENTS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail and deposited with the U.S. Postal Service and addressed to: M/S: NON-FEE AMENDMENTS, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 2231-1450 on

Dated: June 18, 2004

Signature

*Stephen T. Neal*  
Stephen T. Neal

**AMENDMENT AND RESPONSE**

Dear Sir:

The following were sent in response to the office action mailed on March 19, 2004.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Amendments to Specification** begin on page 8 of this paper.

**Remarks/Arguments** begin on page 11 of this paper.